

Search Notes

Application/Control No.

10/773,050

Examiner

Peter Coughlan

Applicant(s)/Patent under
Reexamination

SALAM ET AL.

Art Unit

2129

SEARCHED

Class	Subclass	Date	Examiner
706	1	8/14/2006	PDC
706	15	8/14/2006	PDC
706	45	8/14/2006	PDC
700	1	8/14/2006	PDC
700	90	8/14/2006	PDC
708	622	8/14/2006	PDC
706	16	8/14/2006	PDC
600	586	8/14/2006	PDC
318	254	8/14/2006	PDC

INTERFERENCE SEARCHED

Class	Subclass	Date	Examiner

**SEARCH NOTES
(INCLUDING SEARCH STRATEGY)**

	DATE	EXMR
East--capacitor, quadrant multipliers, transconductance, amplifiers, load resistance, d flip flops, neural networks, Waheed,	8/14/2006	PDC
East--II--Fathi, Erten, feed forward, VLSI, medium, transmission, communication, databus, weights, training, memory, storage,	8/14/2006	PDC
East--III--pattern comparison, analog, digital, continuous time, substrate, interconnection, one dimension, pulsed NN	8/14/2006	PDC
East--IV--bi stable multivibrator, predetermined w/time	8/14/2006	PDC
IEEE--Fathi M. Salam, Khurram Waheed, VLSI, capacitor, quadrant multiplier, d flip flops, bi stable multivibrator, substrate,	8/14/2006	PDC
Inventors Fathi M. Salam Khurram Waheed	8/14/2006	PDC
708.62 with 17 X 16 array, multiplexers	8/14/2006	PDC
600.586 with AE signals, RAM, ROM, VLSI, bus, training, analog to digital	8/14/2006	PDC